

**METHOD FOR TINNING ELECTRONIC PARTS**

Patent Number: JP63118093  
Publication date: 1988-05-23  
Inventor(s): KOBAYASHI KOKICHI  
Applicant(s): TANAKA ELECTRON IND CO LTD  
Requested Patent: JP63118093  
Application Number: JP19860264216 19861105  
Priority Number(s):  
IPC Classification: C25D5/18  
EC Classification:  
Equivalents:

---

**Abstract**

---

**PURPOSE:** To inhibit the formation of whiskers on the tinned surfaces of electronic parts by supplying electric current between an electrode plate of metallic Sn and the electronic part to be tinned in a tinning soln. in opposite directions with a specified period.

**CONSTITUTION:** A tinning soln. (m) is poured in a tinning cell A and an electrode plate 1 of metallic Sn and electronic parts 2 to be tinned are immersed in the soln. (m). The polarity of power sources is changed over with a change-over switch S fitted with a timer. Normal electric current M flowing from the Sn electrode 1 to the parts 2 is first supplied at  $0.2\text{--}3\text{A/dm}^2$  current density for  $t_1$  sec and then reverse electric current N flowing from the parts 2 to the Sn electrode 1 is supplied at  $0.1\text{--}1.5\text{A/dm}^2$  current density for  $(0.01\text{--}0.5)t_1$  sec. The parts 2 are tinned by repeatedly supplying the normal and reverse electric currents and the formation of whiskers on the resulting Sn layers on the surfaces of the parts 2 is prevented.

---

Data supplied from the esp@cenet database - I2